ATTY, DOCKET NO. SERIAL NO. FORM PTO-1449 U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office IADIO L8612.04115 10/669,592 INFORMATION DISCLOSURES AUG 1 8 2006 **APPLICANT** Hisao KOGA, et al. (Use several sheets if necessary) FILING DATE September 23, 2003 **GROUP** 2611 <u>PATENT DOCUMENTS</u> FILING DATE APPROPRIATE FXAMINER DOCUMENT NUMBER DATE NAME CLASS SUBCLASS KCT 6 4 7 3 0 9 10/2002 Malvar KCT 4 8 7 5 7 4 11/2002 6 Malvar KCT 7 9 5 12/2002 6 4 9 6 Malvar **FOREIGN PATENT DOCUMENTS BUBCLASS** DOCUMENT NUMBER DATE COUNTRY CLASS TRANSLATION YES NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) J. ALHAVA, et al.; "Adaptive Sine-Modulated/Cosine-Modulated Filter Bank Equalizer for Transmultiplexers," ECCTD'01-European Conference on Circuit Theory and Design, Aug. 28-31, 2001, Espoo, Finland, pp. III-337-340. KCT **KCT** A. VIHOLAINEN, et al.; "Implementation of Parallel Cosine And Sine Modulated Filters Banks For Equalized Transmultiplexer System," 2001, IEEE, pp. 3625-3628. A, VIHOLAINEN, et al.; "Complex Modulated Critically Sampled Filter Banks Based On Cosine And Sine Modulation," Institute of Communications Engineering, Tampere University of Technology, 2002, IEEE, pp. I-833-836. KCT **KCT** J. ALHAVA, et al.; "Exponentially-Modulated Filter Bank-Based Transmultiplexer," Tampere University of Technology, 2003, IEEE, pp. IV-233-236. KCT J. ALHAVA, et al.: "Efficient Implementation of Complex Exponentially-Modulated Filter Banks," Tampere University of Technology, 2003, IEEE, pp. IV-157-160. Y. YANG, et al.; "DSP Implementation of Low-Complexity Equalizer For Multicarrier Systems," Institute of Communications Engineering, Tampere University of Technology, 2003, IEEE, pp. 271-274. KCT **EXAMINER** DATE CONSIDERED /Khanh Tran/ (09/21/2006)